



*JPW*

Attorney Docket No. SEC.1129  
Application No.10/758,539

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of :  
Gyung-Su Byun : Group Art Unit: 2818  
Application No. 10/758,539 : Examiner: TBA  
Filed: January 16, 2004 :  
For: SEMICONDUCTOR DEVICE INCLUDING DUTY CYCLE CORRECTION  
CIRCUIT

**REQUEST TO CORRECT PTO'S POWER  
OF ATTORNEY RECORDS**

U.S. Patent and Trademark Office  
Customer Service Window  
Randolph Building  
401 Dulany Street  
Alexandria VA 22314

Sir:

The U.S. Patent and Trademark Office records are apparently in error with respect to the power of attorney associated with the above-identified application.

Attached is a copy of the Declaration And Power Of Attorney which is of record in the above-identified application. As shown, power of attorney in the application is vested in the following individuals:

Adam C. Volentine, Reg. No. 33,289  
William S. Francos, Reg. No. 38,456

Please correct the records within the U.S. Patent and Trademark Office to reflect the above-individuals as having power of attorney.

Also, submitted herewith is a Change of Correspondence Address form executed by undersigned, who has power of attorney as noted above.

Respectfully submitted,

Volentine Francos & Whitt, PLLC

A handwritten signature in black ink, appearing to read 'Adam C. Volentine', with a stylized flourish at the end.

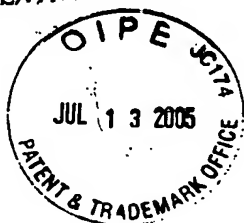
Adam C. Volentine  
Registration No. 33,289

One Freedom Square  
11951 Freedom Drive, Suite 1260  
Reston VA 20190  
Tel. (703) 715-0870

Date: July 13, 2005

VOLENTINE FRANCOS, P.L.L.C. (9/2001)

 **COPY**



## DECLARATION AND POWER OF ATTORNEY FOR U.S. PATENT APPLICATION

☒ Original ☐ Supplemental ☐ Substitute ☐ PCT ☐ Design

As a below named inventor, I hereby declare that: my residence, post office address and citizenship are as stated below next to my name; that I verify believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural inventors are named below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

**TITLE:** SEMICONDUCTOR DEVICE INCLUDING DUTY CYCLE CORRECTION CIRCUIT

### BEST AVAILABLE COPY

of which is described and claimed in:

☒ the attached specification, or

☐ the specification in the application Serial No. \_\_\_\_\_ filed \_\_\_\_\_  
and with amendments through \_\_\_\_\_ (if applicable), or

☐ the specification in International Application No. \_\_\_\_\_ filed \_\_\_\_\_  
and as amended on \_\_\_\_\_ (if applicable).

I hereby state that I have reviewed and understand the content of the above-identified specification, including the claims, as amended by any amendment(s) referred to above.

I acknowledge my duty to disclose information of which I am aware which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, §1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code, §119 (and §172 if this application is for a Design) of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application in which priority is claimed:

COUNTRY	APPLICATION NO.	DATE OF FILING	PRIORITY CLAIMED
Republic of Korea	2003-3295	17 January 2003	YES

I hereby claim the benefit under Title 35, United States code, §120 and §119(c) of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, §1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

APPLICATION SERIAL NO.	U.S. FILING DATE	STATUS; PATENTED, PENDING, ABANDONED

VOLENTINE FRANCOS, P.L.L.C. (9/2001)

And I hereby appoint Adam C. Valentine, Reg. No. 33289 and William S. Francos, Reg. No. 38,456, and the firm of **VOLENTINE FRANCOS, P.L.L.C.**, jointly and severally, attorneys to prosecute this application and to transact all business in the U.S. Patent and Trademark Office connected therewith.

I hereby authorize the U.S. attorneys named herein to accept and following instructions from Y.P. LEE, MOCK & PARTNERS, as to any action to be taken in the U.S. Patent and Trademark Office regarding this application without direct communication between the U.S. attorneys and myself. In the event of a change in the persons from whom instructions may be taken, the U.S. attorneys named herein will be so notified by me.

Kindly direct all correspondence to:

**VOLENTINE FRANCOS, P.L.L.C.**  
12200 Sunrise Valley Drive,  
Suite 150  
Reston, VA 20191

Telephone (703) 715-0870  
Facsimile (703) 715-0877

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FULL NAME OF 1 <sup>st</sup> INVENTOR	FAMILY NAME	FIRST GIVEN NAME	SECOND GIVEN NAME
	BYUN	Gyung-su	
RESIDENCE & CITIZENSHIP	CITY	STATE OR COUNTRY	COUNTRY OF CITIZENSHIP
	Seoul	Republic of Korea	Republic of Korea
POST OFFICE ADDRESS	ADDRESS CITY STATE OR COUNTRY ZIP CODE		
	111-302, Kukdong Apt., Sadang 2-dong, Dongjak-gu, Seoul, Republic of Korea		
FULL NAME OF 2 <sup>nd</sup> INVENTOR	FAMILY NAME	FIRST GIVEN NAME	SECOND GIVEN NAME
RESIDENCE & CITIZENSHIP	CITY	STATE OR COUNTRY	COUNTRY OF CITIZENSHIP
POST OFFICE ADDRESS	ADDRESS CITY STATE OR COUNTRY ZIP CODE		
FULL NAME OF 3 <sup>rd</sup> INVENTOR	FAMILY NAME	FIRST GIVEN NAME	SECOND GIVEN NAME
RESIDENCE & CITIZENSHIP	CITY	STATE OR COUNTRY	COUNTRY OF CITIZENSHIP
POST OFFICE ADDRESS	ADDRESS CITY STATE OR COUNTRY ZIP CODE		

VOLENTINE FRANCO, P.L.L.C. (9/2001)

FULL NAME OF 4TH INVENTOR	FAMILY NAME	FIRST GIVEN NAME	SECOND GIVEN NAME
	CITY	STATE OR COUNTRY	COUNTRY OF CITIZENSHIP
	ADDRESS CITY STATE OR COUNTRY ZIP CODE		
POST OFFICE ADDRESS			
FULL NAME OF 5TH INVENTOR	FAMILY NAME	FIRST GIVEN NAME	SECOND GIVEN NAME
	CITY	STATE OR COUNTRY	COUNTRY OF CITIZENSHIP
	ADDRESS CITY STATE OR COUNTRY ZIP CODE		
POST OFFICE ADDRESS			
FULL NAME OF 6TH INVENTOR	FAMILY NAME	FIRST GIVEN NAME	SECOND GIVEN NAME
	CITY	STATE OR COUNTRY	COUNTRY OF CITIZENSHIP
	ADDRESS CITY STATE OR COUNTRY ZIP CODE		
POST OFFICE ADDRESS			

I further declare that all statements made herein of my own knowledge are true, and that all statements on information and believe are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

1<sup>st</sup> Inventor Byun Gyung-su Date 01/24/04  
 Gyung-su BYUN  
 2<sup>nd</sup> Inventor \_\_\_\_\_ Date \_\_\_\_\_  
 3<sup>rd</sup> Inventor \_\_\_\_\_ Date \_\_\_\_\_  
 4<sup>th</sup> Inventor \_\_\_\_\_ Date \_\_\_\_\_  
 5<sup>th</sup> Inventor \_\_\_\_\_ Date \_\_\_\_\_  
 6<sup>th</sup> Inventor \_\_\_\_\_ Date \_\_\_\_\_

Applicant's Ref. SS-18493-US Attorney Docket No. SEC.1129